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Art Unit: 2826

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From: Winston Hsu, Registration No. 41,526

Serial No.: 10/711,794

Attorney Docket No.: OSEP0009USA

Subject: Information Disclosure Statement (IDS)

Total Pages: 49 pages (including cover page)

Winston Hsu 2005/12/30

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PTO/SB/21 (09-04)

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	Application Number	10/711,794					
TRANSMITTAL	Filing Date	10/06/2004					
FORM	First Named Inventor	Kuo-Yang Sun					
	Art Unit	2826					
(to be used for all correspondence after initial filling	Examiner Name	MANDALA, VICTOR A					
Total Number of Pages in This Submission	48 Attorney Docket Number	OSEP0009USA					
ENCLOSURES (Check all that apply)							
Fee Transmittal Form  Fee Attached  Amendment/Reply  After Final  Affidavits/declaration(s)  Extension of Time Request  Express Abandonment Request  Information Disclosure Statement  Certified Copy of Priority Document(s)  Reply to Missing Parts/ Incomplete Application  Reply to Missing Parts under 37 CFR 1.52 or 1.53	Drawing(s)  Licensing-related Papers  Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence Ad Terminal Disclaimer Request for Refund CD, Number of CD(s)  Landscape Table on CD  Remarks	After Allowance Communication to TC  Appeal Communication to Board of Appeals and Interferences  Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)  Proprietary Information					
SIGNATU	IRE OF APPLICANT, ATTOR	NEY, OR AGENT					
Firm Name North America Intellectu	al Property Corporation						
Signature Wurken Hases							
Printed name Winston Hsu							
Date 12/30/2005	eg. No. 41,526						
CERTIFICATE OF TRANSMISSION/MAILING							
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:  Stanature							
Typed or printed name Cindy Lin	0	Oate 12/30/2005					

This collection of Information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case, Any comments on the amount of time you require to complete this form and/or suggestions for retaining this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box .1450, Alexandria, VA .22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA .22313-1450.

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PTO/SB/17 (12-04v2)
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[				Examiner Nam	e I	MANDAL	A, VIC	TOR A
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Utility	300	150	500	250	200	100		
Design	200	100	100	50	130	65		
Plant	200	100	300	150	160	80		**************************************
Reissue	300	150	500	250	600	300		
Provisional	200	100	0	0	0	0		
2. EXCESS CLAIM FEES  Fee Description  Each claim over 20 (including Reissues)  Each independent claim over 3 (including Reissues)  Each independent claim over 3 (including Reissues)  Small Entity Fee (\$)  25  20  100								
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3. APPLICATI If the specification listings un	ON SIZE FEE ation and drawing der 37 CFR 1.52(	ms paid for, if greater the s exceed 100 sheet e)), the application See 35 U.S.C. 416	ts of paper size fee o	due is \$250 (\$	125 for sr	nall entity	for each	ch additional 50
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4. OTHER FEE(S)  Non-English Specification, \$130 fee (no small entity discount)  Fees Paid (\$)								
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Signature	10115	stroll		egistration No.	41,526	T	eiephone	3027291562
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Complete if Known Substitute for form 1449/PTO Application Number 10/711,794 Filing Date 10/06/2004 INFORMATION DISCLOSURE First Named Inventor Kuo-Yang Sun STATEMENT BY APPLICANT Art Unit 2826 (Use as many sheets as necessary) **Examiner Name** MANDALA, VICTOR A Attorney Docket Number OSEP0009USA Sheet of

	<u> </u>		U.S. PATENT D	OCUMENTS	
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
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Examiner	Date	
Signature	Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspte.gov">www.uspte.gov</a> or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English

language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1480, Alexandria, VA 22313-1450.

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**PATENT** 

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Kuo-Yang Sun

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Chia-Ming Yang

Application No.:

10/711,794

Filing Date:

10/06/2004

Examiner:

Mandala, Victor A

Art Unit:

2826

10 Docket No.:

OSEP0009USA

Title:

PACKAGE STRUCTURE

To:

Commissioner for Patents

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P.O. BOX 1450

Alexandria, VA 22313-1450

Subject:

Information disclosure statement under 37 CFR §1.56

20 Dear Sir,

This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 CFR §1.56. The applicant wishes to make of record the documents listed on the accompanying form PTO/SB/08.

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Since this IDS is filed after the mailing date of the first Office action but before final Office action, consideration of the information disclosure statement is hereby requested according to 37 CFR §1.97(c).

That each item of information contained in the information disclosure statement was
first cited in an Office communication mailed on October 20, 2005 for the counterpart
Taiwan patent application number 093119793, which are no more than three months
prior to the filing of the information disclosure statement.

From: 8064986673 To: USPTO Page: 6/49 Date: 2005/12/30 下午 02:06:30

According to the requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609, the applicants are submitting an English language abstract of the cited Taiwanese patent TW 456,015 (published Sept 21, 2001). The English language title and abstract of TW 456,015 are listed as follows:

Title: Package with passive components integrated with lead frame

Abstract:

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The object of the present invention is to provide a package with passive components integrated with the lead frame in order to reduce the occupied area by the PCB and reduce the conductive paths, and provide a semiconductor package with increased electrical characteristics. The features are: in the appropriate locations at the periphery of the die pad for the die, configuring conductive protrusions outward from the die pad and making the protrusions and the die pad have the same potential; using the protrusions and the leads of the lead frame to form opposed electrical contact; and, the positive pole and the negative pole of the passive components are across between the protrusion and the corresponding leads of the lead frame to form the electric connections.

According to the requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609, the applicants are submitting an English language abstract of the cited Taiwanese patent TW 567,598 (published Dec 21, 2003). The English language title and abstract of TW 567,598 are listed as follows:

Title: Flip chip semiconductor package

30 Abstract:

A flip chip semiconductor package mainly comprises a semiconductor chip and a

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lead frame having a die pad and a plurality of leads. The semiconductor chip has an active surface and a plurality of pads formed thereon. A plurality of bumps are formed on the pads and electrically connected with the leads and the die pad in order to prevent the die from shifting and provide another path for thermal dissipation and transmitting ground signals. Similarly, the bumps also can connect with the leads and tie bars in order to affix the die onto the lead frame more firmly.

It is respectfully requested that the examiner can consider the document(s) listed on the accompanying form PTO/SB/08 and that it be made of record in the application. The applicants sincerely hope that the examiner initials the cited reference(s) on the form and that a copy of the initialed form be sent to the applicants with the next communication from the examiner.

Date:

December 30, 2005

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Respectfully submitted,

20 Winston Hsu, Patent Agent No. 41,526

Winten Han

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